	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	IS&R	L2	235	(340/572.3).CCLS.	US-PGPUB; USPAT	2005/03/04 13:03			
2	IS&R	L3	232	(361/149).CCLS.	US-PGPUB; USPAT	2005/03/04 13:04			
3	BRS	L4	21	(2 3) and ((zero adj crossing$1) (zero$1crossing$1))	US-PGPUB; USPAT	2005/03/04 13:48			
4	BRS	L5	3150	((zero adj crossing$1) (zero$1crossing$1)) same (processor$1 micro$1processor$1 computer$1 micro$1controller$1)	US-PGPUB; USPAT	2005/03/04 14:03			
5	BRS	L6	1239	5 same (delay$1 interval$1 period$1)	US-PGPUB; USPAT	2005/03/04 13:49			
6	BRS	L7	4	4 and 6	US-PGPUB; USPAT	2005/03/04 13:49			
7	BRS	L8	4094	((zero adj crossing$1) (zero$1crossing$1)) near10 (delay$1 interval$1 period$1)	US-PGPUB; USPAT	2005/03/04 13:56			
8	BRS	L9	3	4 and 8	US-PGPUB; USPAT	2005/03/04 14:02			
9	BRS	L10	1062	5 and 8	US-PGPUB; USPAT	2005/03/04 14:02			
10	BRS	L11	1300	((zero adj crossing$1) (zero$1crossing$1)) near10 (processor$1 micro$1processor$1 computer$1 micro$1controller$1)	US-PGPUB; USPAT	2005/03/04 14:03			
11	BRS	L12	574	8 and 11	US-PGPUB; USPAT	2005/03/04 14:03			
12	BRS	L13	277	8 same 11	US-PGPUB; USPAT	2005/03/04 14:03			
13	BRS	L14	7	(2 3) and (8 11)	US-PGPUB; USPAT	2005/03/04 14:03			